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Statement

In the following paper, which is part of the Habilitation of Dr. Rafał Długosz my participation can be assessed as follows:

No.	Paper title	Participation
1	R. Długosz, G. Fischer, "Low Chip Area, Low Power Dissipation, Programmable, Current Mode, 10-bits, SAR ADC Implemented in the CMOS 130nm Technology", <i>International Conference Mixed Design of Integrated Circuits and Systems (MIXDES)</i> , Gdynia, Poland, June 2015	10 %

Detailed description of the participation in the paper:

- Discussions concerning CMOS implementation issues of the analog-to-digital converter proposed and designed by Dr. Rafał Długosz.
- Consultations during preparation of the paper.



Gunter Fischer